

isc Silicon NPN Power Transistor

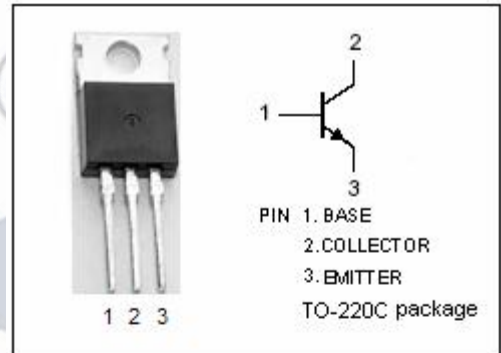
3DG13007

DESCRIPTION

- Collector–Emitter Sustaining Voltage
: $V_{CEO(SUS)} = 400V(\text{Min.})$
- Collector Saturation Voltage
: $V_{CE(sat)} = 2.0(\text{Max}) @ I_C = 5.0A$
- Switching Time
: $t_f = 0.9 \mu s(\text{Max.}) @ I_C = 5.0A$

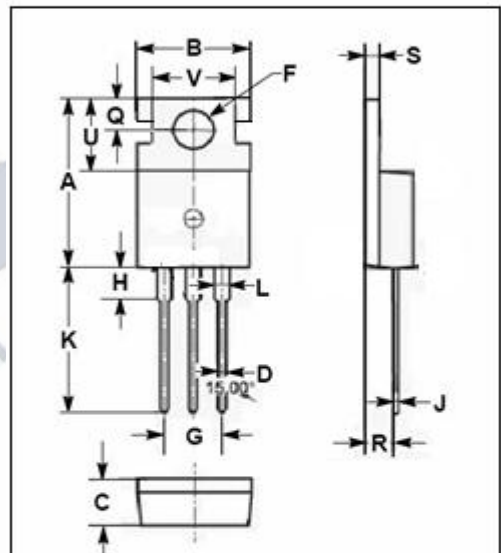
APPLICATIONS

- Designed for use in high-voltage, high-speed, power switching in inductive circuit, they are particularly suited for 115 and 220V switchmode applications such as switching regulators, inverters, Motor controls, Solenoid/Relay drivers and deflection circuits.



ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CEV}	Collector-Emitter Voltage	700	V
V_{CEO}	Collector-Emitter Voltage	400	V
V_{EBO}	Emitter-Base Voltage	9	V
I_C	Collector Current-Continuous	8	A
I_{CM}	Collector Current-peak	16	A
I_B	Base Current	4	A
I_{BM}	Base Current-Peak	8	A
I_E	Emitter Current	12	A
I_{EM}	Emitter Current-Peak	24	A
P_C	Collector Power Dissipation $T_C=25^\circ C$	80	W
T_i	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature Range	-65~150	$^\circ C$



DIM	mm	
	MIN	MAX
A	15.50	15.90
B	9.80	10.20
C	4.20	4.50
D	0.70	0.90
F	3.40	3.70
G	4.98	5.18
H	2.68	2.90
J	0.44	0.60
K	12.80	13.40
L	1.20	1.45
Q	2.70	2.90
R	2.30	2.70
S	1.29	1.35
U	6.45	6.65
V	8.66	8.86

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal Resistance, Junction to Case	1.56	$^\circ C/W$
$R_{th\ j-a}$	Thermal Resistance, Junction to Ambient	62.5	$^\circ C/W$

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ELECTRICAL CHARACTERISTICS

 $T_C = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CE0(SUS)}$	Collector-Emitter Sustaining Voltage	$I_C = 10\text{mA}; I_B = 0$	400			V
$V_{CE(sat)-1}$	Collector-Emitter Saturation Voltage	$I_C = 2\text{A}; I_B = 0.4\text{A}$			1.0	V
$V_{CE(sat)-2}$	Collector-Emitter Saturation Voltage	$I_C = 5\text{A}; I_B = 1\text{A}$ $T_C = 100^\circ\text{C}$			2.0 3.0	V
$V_{CE(sat)-3}$	Collector-Emitter Saturation Voltage	$I_C = 8\text{A}; I_B = 2\text{A}$			3.0	V
$V_{BE(sat)-1}$	Base-Emitter Saturation Voltage	$I_C = 2\text{A}; I_B = 0.4\text{A}$			1.2	V
$V_{BE(sat)-2}$	Base-Emitter Saturation Voltage	$I_C = 5\text{A}; I_B = 1\text{A}$ $T_C = 100^\circ\text{C}$			1.6 1.5	V
I_{CES}	Collector Cutoff Current	$V_{CES} = 700\text{V}; V_{BE(off)} = 1.5\text{V}$ $T_C = 125^\circ\text{C}$			0.1 1.0	mA
I_{EBO}	Emitter Cutoff Current	$V_{EB} = 9\text{V}; I_C = 0$			0.1	mA
h_{FE-1}	DC Current Gain	$I_C = 2\text{A}; V_{CE} = 5\text{V}$	8		40	
h_{FE-2}	DC Current Gain	$I_C = 5\text{A}; V_{CE} = 5\text{V}$	5		30	
f_T	Current-Gain—Bandwidth Product	$I_C = 0.5\text{A}; V_{CE} = 10\text{V};$	4			MHZ
C_{OB}	Output Capacitance	$I_E = 0; V_{CB} = 10\text{V}; f_{test} = 0.1\text{MHz}$		80		pF

Switching Times; Resistive Load

t_d	Storage Time	$I_C = 5\text{A}; V_{CC} = 125\text{V};$ $I_{B1} = I_{B2} = 1\text{A}; t_p = 25\ \mu\text{s};$ Duty Cycle $\leq 1\%$			0.1	μs
t_r	Fall Time				1.5	μs
t_s	Storage Time				3.0	μs
t_f	Fall Time				0.7	μs